

ELECTRIC DOUBLE LAYER CAPACITORS

PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): (日期):2018-06-15

CATEGORY (品名) : ELECTRIC DOUBLE LAYER CAPACITORS

DESCRIPTION (型号) : DRL 2.7V15F (φ12.5X25)

VERSION (版本) : 01

Customer P/N : /

SUPPLIER : /

SUPPLIER					
PREPARED (拟定)	CHECKED (审核)				
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CUSTOMER				
APPROVAL (批准)	SIGNATURE (签名)			

	SPECIFICATION				ALTERNATION HISTORY		
	DRL SERIES					ECORDS	
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

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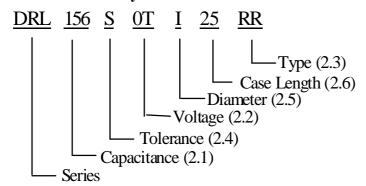
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1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

2. Part Number System



2.1 <u>Capacitance code</u>

Code	156
Capacitance (F)	15

2.2 Rated voltage code

Code	0T
Voltage (W.V.)	2.7

2.3 <u>Type</u>

Code	RR
Type	Bulk

2.4 <u>Capacitance tolerance</u>

"S" stands for $-20\% \sim +50\%$

2.5 <u>Diameter</u>

Code	I
Diameter	12.5

2.6 <u>Case length</u> 25=25mm

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3. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as

follows:

Air Pressure

Ambient temperature: 15°C to 35°C Relative humidity : 25% to 75% : 86kPa to 106kPa

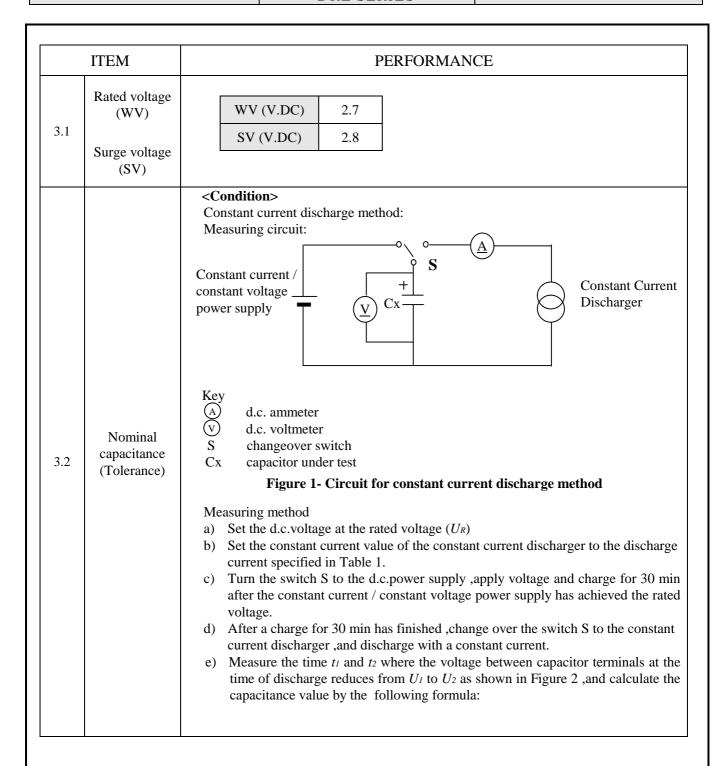
If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature: $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70% Air Pressure : 86kPa to 106kPa

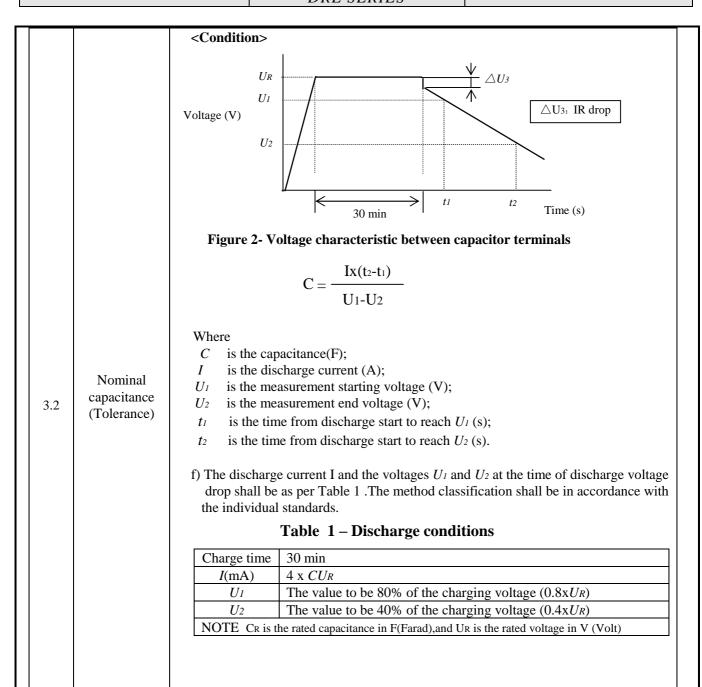
Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40°C to 70°C.

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		Measuring Measuring <criteria:< th=""><th>frequency temperature point</th><th>::20±2℃ : 2mm max fi</th><th>rom the surface of</th><th>a sealing resin on the lead win</th></criteria:<>	frequency temperature point	::20±2℃ : 2mm max fi	rom the surface of	a sealing resin on the lead win								
3.3	ESR	Rated V (V	oltage C	apacitance (F)	Dimension (D×L, mm)	ESR, AC (m Ω) (max) at 1kHz/20°C								
		2.7	7	15	12.5X25	80								
3.4	Leakage current	1. Ambien 2.The elec 3. Desistar <criteria> Less than t I≤ 0.030m</criteria>	<condition> 1. Ambient temperature: 25°C ± 2°C. 2. The electrification time:72H 3. Desistance value of protective resistor less than 1Ω. <criteria> Less than the initial limit(25°C ± 2°C): I≤ 0.030mA I is the Leakage current</criteria></condition>											
		<condition< td=""><td></td><td>rature(°C)</td><td>Item</td><td>Characteristics</td></condition<>		rature(°C)	Item	Characteristics								
		1)±2	Capacitance SESR									
	Temperature	-	_				△C/C	Within ±30% of initial capacitance						
				_	_	_	_	_	_	2	-4	0+3	ESR	Less than or equal to 4 times of the value of item 3.3
3.5										-	_	_	_	_
	characteristic	4	71	0±2	△C/C	Within ±30% of initial capacitance								
		4	,	∪— ∠	ESR	The limit specified in 3.3								
		1kHz;												

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		<criteria></criteria>	nours .The result should meet the following table:
		Item	Performance
		Capacitance Change	Within ±30% of initial capacitance
3.6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
3.0	test	Appearance	No visible damage and no leakage of electrolyte
		-	exposed for 240±48 hours in an atmosphere of 90~95%RH stic change shall meet the following requirement.
		<criteria></criteria>	
		Item	Performance
	Damp	Capacitance Change	Within ±30% of initial capacitance
3.7	heat test	ESR	Less than or equal to 4 times of the value of item 3.3
3.1		Appearance	No visible damage and no leakage of electrolyte

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		a) Lead pull strength A static load force shall b in a direction away from	1.1	erminal in the axial direction and acting
		Lead wire diame	Load force (N)	
		0.5 < d ≤0.8	,	10
3.8	Lead strength	table above is applied to on horizontal position and the for 2~3 seconds. The additional bends are in Lead wire diameter. 0.5 < d \le 0.8	made in the oppo er (mm) eristic shall meet Performanc Within ±30	Load force (N) 5 the following value after a) or b) test. e % of initial capacitance damage Legible marking and no
3.9	Resistance to vibration	capacitance when the value is	Fig2 s shall not show measured within	

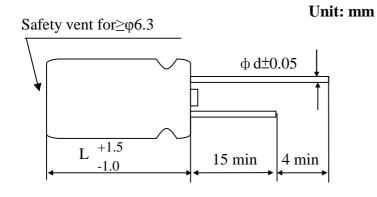
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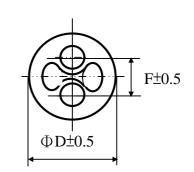
	1	
3.10	Solderability	The capacitor shall be tested under the following conditions: Solder : Sn-3Ag-0.5Cu Soldering temperature: 245±3°C Immersing time : 2.0±0.5s Immersing depth : 1.5~ 2.0mm from the root. Flux : Approx .25% rosin (JIS K5902) in ETHANOL (JIS K1501) Performance: At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	A) Solder bath method Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylaehol solution of colophony. Then it will be immersed at the surface of the solder with the following condition: Solder : Sn-3Ag-0.5Cu Soldering temperature : 260±5°C Immersing time : 5±0.5s Heat protector: t=1.6mm glass -epoxy board B) Soldering iron method Bit temperature : 350±10°C Application time : 3.5±0.5 s Heat protector: t=1.6mm glass -epoxy board For both methods, after the capacitor at thermal stability, the following items shall be measured: Item Performance Capacitance Change Within±10% of initial capacitance Appearance No visible damage legible marking and no leakage of electrolyte

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4. Product Dimensions





φD	12.5
L	25
F	5.0
φd	0.6

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	B.T. 4.	• 4	
5 .	Noti	ce item	

- (1) The capacitor has fixed polarity.
- (2) The capacitor should be used under rated voltage.
- (3) The capacitor should not be used in the charge and discharge circuit with high frequency.
- (4) The ambient temperature affects the super capacitor life.
- (5) Voltage reduction $\Delta V=IR$ will happen at the moment of discharge.
- (6) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.
- (7) The capacitor should stored in the environment within -30°C ~50°C temperature and less than 60% relative humidity.
- (8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.
- (9) Don't twist capacitor or make it slanting after installing.
- (10) Need avoid over heat on the capacitor during soldering (The temperature should be 260° C with the time less than 5s during soldering on 1.6mm printed PCB.)
- (11) There is voltage balance problem between each capacitor unit during series connection between super capacitor.

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